

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3463	test routine\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:36
L2	22565	internal memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:41
L3	172	I1 and I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:41
L4	4116	processor core	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:34
L6	153	GPIO interface or general purpose input/output interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:43
L7	49837	ASIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:43
L8	825598	processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:44
L9	135	I3 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:44

L10	1	I9 and I6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:44
L11	1	I10 and I7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:44
L12	10	I9 and I7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:46
L13	392	I2 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:46
L14	4	I13 and I6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:13
L15	0	I14 and I1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 14:46
L16	4032	test vector\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:13
L17	374	I2 and (I1 or I16)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:13

L18	75	I17 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:13
L19	2	I18 and I6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:14
L20	70	I18 and I7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:14
L21	960243	interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:18
L23	0	processing core and (execut\$3 test routine\$1 stored near2 integrated circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:33
L24	0	execut\$3 and ( test routine\$1with integrated circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:33
L25	0	execut\$3 and ( test routine\$1with internal memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:34
L26	75	processor core and I17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:35

L27	75	I21 and I26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:35
L28	4	activat\$3 and (first output signal with test result\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 15:40
L30	24	"5668815"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 16:13
L31	504638	integrated circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 16:13
L33	976	I1 and I31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 16:17
L34	96	I33 and I17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 16:14
L35	3	I16 and I34	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 16:14
L36	78	I34 and I21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 16:17

L37	1	activat\$3 and (first output signal near2 test result\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 16:18
L38	114174	test result\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 16:18
L39	11	I38 and I36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2004/12/24 16:19